

01/12/02



JC928 U.S. PTO

TRANSMITTAL FORM

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail" under Label No. EL 886 594 359 US in an envelope addressed to: Box Patent Application, Assistant Commissioner for Patents, Washington, D.C. 20231 on:

January 12, 2002
(Date of Deposit)

Kathy Dixon



Box Patent Application
Assistant Commissioner
For Patents
Washington, D.C. 20231

Attorney Doc. #: 67,200-591
Mailing Date: Jan. 12, 2002

Dear Sir:

Transmitted herewith for filing is the patent application of:

Inventor(s): Hsin-Hui Lee Chia-Fu Lin
 Chao-Yuan Su Yen-Ming Chen
 Kai-Ming Ching Li-Chih Chen

For: Method of Making a Wafer Level Chip Scale Package

Submitted herewith are:

- ☒ 5 sheets of formal drawings showing Figs 1-4L
- ☒ An Assignment of the invention to Taiwan Semiconductor Manufacturing Co., Ltd., together with Assignment Recordal Sheet
- ☒ A Declaration for patent application under CFR 1.63 and 1.68

The filing fee has been calculated as shown below:

	No. Filed	No. Extra	Small Entity Fee	Large Entity Fee	Total
Basic Fee			\$370.00	\$740.00	\$740.00
Total Claims	20x20	0 x	\$9.00	\$18.00	\$0
Indep. Claims	2 -3	0 x	\$42.00	\$84.00	\$0
Multiple Dep. Clms.			\$140.00	\$280.00	\$0
Assign. Rec. Fee			\$40.00	\$40.00	\$40.00
TOTAL					\$780.00

Mailing Date: Jan-12, 2002
Attorney Docket No.: 67,200-591

X Attached is a Credit Card Payment Form for the sum of \$ 780.00


X Charge Visa Credit Card No. 4756 8461 9568 0263 the sum of \$ 780.00
A duplicate copy of this transmittal is attached.

X The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Visa Credit Card No. 4756 8461 9568 0263.
A duplicate copy of this sheet is enclosed.

X Any additional filing fees required under 37 CFR 1.6
X Any patent application processing fees under 37 CFR 1.17

Respectfully submitted,

TUNG & ASSOCIATES



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RWT/kd
Enclosures